

**In the Claims:**

Please amend the claims as follows:

26. (Twice Amended) A digital communications transmitter circuit as claimed in claim 24 wherein:

    said filtered signal stream is a quadrature signal stream having a locus that passes proximate one of said phase points of said phase-point constellation at integral unit baud intervals;

    said first filtered-signal data stream comprises on-time samples of said filtered signal stream, each of said on-time samples occurring substantially coincidentally with said passage of said filtered signal locus proximate one of said phase points of said phase-point constellation; and

    said second filtered-signal data stream comprises off-time samples of said filtered signal stream wherein each of said off-time samples occurs between adjacent ones of said on-time samples.

52. (Twice Amended) A digital communications transmitter circuit as claimed in claim 6 wherein:

    said filtered signal stream is a stream of complex digital values, with each of said complex digital values exhibiting a peak magnitude component; and

    said constrained-envelope generator is configured to determine when ones of said peak magnitude components exceed a threshold value.

**STATUS OF CLAIMS**

1: Canceled.

2-4: Pending.

5: Canceled.

6-8: Pending

9: Canceled.

10: Pending.

11-13: Canceled.

14: Pending.

15-18: Canceled.

19, 20: Pending.

21-23: Canceled.

24-29: Pending.

30-51: Canceled.

52-55: Pending.

56-57: Canceled.

58-59: Pending.

60: Canceled.

61-64: Pending.